



NEWS Letter

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ICT- Basics of Computer Networks

08.01.2026

Mr. Pradeep Kumar, RN Computers, Director

Organised by -Department of Artificial Intelligence & Data Science

Jeppiaar Institute of Technology, Chennai, Tamil Nadu

Introduction:

The Department of Artificial Intelligence & Data Science, Jeppiaar Institute of Technology, organized a seminar on “Basics of Computer Networks and Technological Innovation” on 08 December 2025. The seminar was conducted to strengthen students’ understanding of fundamental networking concepts and to provide insights into recent technological innovations in the field of computer networks.

Objective of the Seminar:

The main objectives of the seminar were:

- To introduce students to the fundamental concepts of computer networks
- To enhance awareness of emerging technologies and innovations in networking
- To bridge the gap between theoretical knowledge and practical applications
- To motivate students towards continuous learning in advanced networking domains



About the Speaker:

Mr. Pradeep Kumar, has rich academic experience in computer networks and related technologies. His expertise and effective presentation style helped students gain a clear understanding of networking fundamentals and current technological trends.

Program Details:

During the seminar, the speaker explained core concepts such as network types, network topologies, transmission media, protocols, and networking devices. He also highlighted recent technological innovations and their impact on modern communication systems. The session was interactive, with students actively participating and clarifying their doubts through questions and discussions.

Participation:

- Students of the Department of Artificial Intelligence & Data Science**
- Faculty members of the department**

Outcome of the Program

The seminar provided students with a strong foundation in computer networks and enhanced their understanding of technological innovations. Participants gained valuable insights that will help them in academic learning, project work, and future career development.

Conclusion:

The seminar on Basics of Computer Networks and Technological Innovation was highly informative and beneficial for the students. The Department of Artificial Intelligence & Data Science sincerely thanks Mr. Pradeep Kumar for sharing his knowledge and expertise. The program successfully met its objectives and contributed to the academic enrichment of the students.

Program Title: Memorandum of Understanding (MoU)

Date: 08.01.2026

Organised by:

Department of Artificial Intelligence & Data Science

Jeppiaar Institute of Technology, Chennai, Tamil Nadu

The Department of Artificial Intelligence & Data Science, Jeppiaar Institute of Technology, Chennai, successfully entered into a Memorandum of Understanding (MoU) with RN COMPUTERS Private Limited on 08 January 2026.

The primary objective of this MoU is to strengthen academic-industry collaboration and bridge the gap between theoretical knowledge and practical industry requirements. Through this partnership, students will gain access to industry-relevant training, hands-on workshops, internships, industrial projects, expert lectures, and skill development programs in emerging areas of Artificial Intelligence and Data Science.

The collaboration also aims to enhance faculty exposure to current industrial practices and technologies, promote joint research activities, and support curriculum enrichment aligned with industry standards. This MoU is expected to significantly contribute to improving students' employability, professional competence, and real-world problem-solving skills, thereby fostering a mutually beneficial relationship between the institution and the industry partner.

Scope of Collaboration

The MoU facilitates collaboration in the following areas:

- Conducting guest lectures, workshops, seminars, and webinars by industry experts**
- Organizing industry-oriented training and certification programs**
- Providing guidance for mini projects, final-year projects, and internships**
- Joint participation in research and development activities**
- Organizing hackathons, boot camps, and technical events**
- Supporting career guidance and placement-related activities, wherever feasible**



Program Highlights:

- Formal signing of the MoU between RN Computers. and the Department of AI & DS
- Discussion on emerging trends in Artificial Intelligence and Data Science
- Interaction between faculty members and industry representatives
- Identification of future collaborative academic and training activities

Benefits to Students and Faculty:

- Exposure to real-world industry practices and tools
- Opportunities for hands-on training and project-based learning
- Faculty development through industry interaction and knowledge sharing
- Enhanced prospects for internships, certifications, and placements

Outcome of the Program:

The MoU marks a significant step towards strengthening industry–academia partnerships. It is expected to contribute positively to student skill development, faculty enrichment, and collaborative research initiatives, thereby improving the overall academic and professional growth of the institution.

Program Title: Memorandum of Understanding (MoU)

Date: 22.01.2026

Organised by:

Department of Artificial Intelligence & Data Science

Jeppiaar Institute of Technology, Chennai, Tamil Nadu

The Department of Artificial Intelligence & Data Science, Jeppiaar Institute of Technology, Chennai, successfully entered into a Memorandum of Understanding (MoU) with Coirei on 22 January 2026. Coirei is a deep-technology, product-focused company engaged in building long-lasting, research-driven software and platform solutions across emerging domains such as Artificial Intelligence, Quantum Technologies, Web4 architectures, and next-generation intelligent systems.

The objective of this MoU is to foster strong academic-industry collaboration and provide students with exposure to cutting-edge technologies and real-world industry practices. The partnership aims to facilitate industry-oriented training programs, technical workshops, internships, live projects, expert lectures, and collaborative research activities in advanced technology areas.

This collaboration will also support curriculum enrichment, faculty development programs, and joint innovation initiatives, ensuring alignment with rapidly evolving industry requirements. The MoU is expected to enhance students' technical competencies, research capabilities, and employability, while promoting innovation and knowledge exchange between the institution and the industry partner.

The signing of this MoU marks a significant step towards strengthening the department's commitment to excellence in education, research, and industry engagement.



Scope of Collaboration:

The MoU facilitates collaboration in the following areas:

- **Conducting guest lectures, workshops, seminars, and webinars by industry experts**
- **Organizing industry-oriented training and certification programs**
- **Providing guidance for mini projects, final-year projects, and internships**
- **Joint participation in research and development activities**
- **Organizing hackathons, boot camps, and technical events**
- **Supporting career guidance and placement-related activities, wherever feasible**

Program Title : “Seminar on Quantum Computing”

 **22.01.2026**

 **Mr. Naveen Kumar Director- Coirei**

**Organised by -Department of Artificial Intelligence & Data Science
Jeppiaar Institute of Technology, Chennai, Tamil Nadu**

The Department of Artificial Intelligence & Data Science, Jeppiaar Institute of Technology, Chennai, successfully organized a Seminar on “Quantum Computing” on 22 January 2026. The seminar was delivered by Mr. Naveen Kumar, Director of Coirei, who provided valuable insights into the fundamental principles and emerging applications of quantum computing.

The session introduced students to key concepts such as qubits, superposition, entanglement, and quantum algorithms, highlighting how quantum computing differs from classical computing paradigms. The resource person also discussed the current state of quantum technologies, real-world use cases, and future research directions across areas such as cryptography, optimization, artificial intelligence, and complex system simulations.

Participants gained awareness of industry trends, research opportunities, and career pathways in the rapidly evolving field of quantum computing. The seminar encouraged students to explore interdisciplinary learning and advanced research, aligning with next-generation technological developments.

The program concluded with an interactive question-and-answer session, and the seminar was highly beneficial in enhancing students’ understanding of quantum technologies and their impact on future digital systems.



Program Title: Seminar on AI & Innovation Sprints : Rapid Prototyping for Digital Transformation

Date: 27.01.2026

✍ Mr. M. Asan Nainar Asst Professor, SRM Valliammal Engineering College

Organised by:

**Department of Artificial Intelligence & Data Science
Jeppiaar Institute of Technology, Chennai, Tamil Nadu**

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INOVATION COUNCIL
AWS
DEALab

DEPARTMENT OF ARTIFICIAL INTELLIGENCE AND DATA SCIENCE
SEMINAR ON: AI & INNOVATION SPRINTS: RAPID PROTOTYPING FOR DIGITAL TRANSFORMATION
RESOURCE PERSON: Mr. M. ASAN NAINAR M.Tech. (CSE)
Assistant Professor (Selection Grade)
SRM VALLIAMMAI ENGINEERING COLLEGE

CHIEF PATRON
DR. N MARIE WILSON
CHAIRMAN

IQAC DIRECTOR
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PATRON
DR. J VENU GOPALAKRISHNAN
PRINCIPAL

HEAD OF THE DEPARTMENT
MR. S SARAVANAN
ASSISTANT PROFESSOR

FACULTY CO-ORDINATOR
DR. G ARUTPERUMJOTHI
ASSISTANT PROFESSOR

DATE: 27/01/2026
DAY: TUESDAY
VENUE: HALL NO 28
TIMING: 2PM TO 3PM
MODE: ONLINE

Link: <https://tinyurl.com/43ua2t62>

The Department of Artificial Intelligence & Data Science, Jeppiaar Institute of Technology, Chennai, successfully organized a Seminar on “AI & Innovation Sprints: Rapid Prototyping for Digital Transformation” on 27 January 2026. The seminar was delivered by Mr. M. Asan Nainar, Assistant Professor, SRM Valliammal Engineering College, who shared valuable insights into the role of Artificial Intelligence in accelerating innovation and digital transformation.

The seminar focused on the concept of innovation sprints, emphasizing rapid ideation, prototyping, and iterative development using AI-driven tools and methodologies. The resource person highlighted real-world case studies demonstrating how organizations leverage AI-enabled rapid prototyping to develop scalable digital solutions within shorter development cycles.

Participants gained exposure to design thinking approaches, agile innovation frameworks, and practical strategies for implementing AI solutions in business and engineering contexts. The session encouraged students to think creatively and adopt a problem-solving mindset aligned with current industry and startup ecosystems.

The seminar concluded with an interactive discussion, during which students clarified their queries related to AI innovation practices, product development, and emerging career opportunities. The program was highly informative and contributed significantly to enhancing students' understanding of AI-driven digital transformation and innovation methodologies.



Program Title : “Workshop on Digital System Design using Altera FPGA”

 **28.01.2025**

 **Dr. K. Padmanaban, Altera Semiconductor Technology India Pvt Ltd**

Organised by -Department of Artificial Intelligence & Data Science

Jeppiaar Institute of Technology, Chennai, Tamil Nadu

The Department of Artificial Intelligence & Data Science, Jeppiaar Institute of Technology, Chennai, successfully organized a Workshop on Digital System Design using Altera FPGA on 28 January 2025. The workshop was delivered by Dr. K. Padmanaban, Altera Semiconductor Technology India Pvt. Ltd., an expert in FPGA-based system design and semiconductor technologies.

The objective of the workshop was to provide students with practical knowledge of digital system design concepts, FPGA architecture, and real-time implementation using Altera FPGA platforms. The resource person explained the fundamentals of digital design, HDL-based implementation, synthesis, simulation, and hardware testing using industry-standard tools.

The session was highly interactive and included hands-on demonstrations, enabling students to gain practical exposure to FPGA programming and digital circuit implementation. The workshop enhanced students' understanding of hardware–software co-design and its applications in AI, embedded systems, and high-performance computing.

The program concluded with an interactive discussion, where students clarified their queries related to FPGA design and career opportunities in the semiconductor industry. The workshop was highly beneficial and received positive feedback from the participants.

About the Speaker

Dr. K. Padmanaban is a distinguished professional from Altera Semiconductor Technology India Pvt. Ltd., with extensive expertise in digital system design, FPGA architectures, and semiconductor technologies. He has rich industry experience in designing and implementing high-performance digital systems using Altera FPGA platforms for real-time and industrial applications.

Dr. Padmanaban has been actively involved in research, development, and training in the areas of HDL-based design, embedded systems, hardware acceleration, and system optimization. He has delivered numerous technical talks, workshops, and hands-on training sessions for students, faculty members, and industry professionals, focusing on bridging the gap between academic concepts and industry practices.

His strong commitment to knowledge sharing and skill development has significantly contributed to preparing students for careers in the semiconductor and VLSI domains. His sessions are known for their clarity, practical orientation, and relevance to current industry trends.

Participation

- Students of the Department of Artificial Intelligence & Data Science
- Faculty members of the department

Outcome of the Program

Workshop on Digital System Design using Altera FPGA

At the end of the workshop, the participants:

- Understand the fundamental concepts of digital system design.
- Gain practical knowledge in using Altera FPGA development tools.
- Develop skills in HDL (Verilog/VHDL) for digital logic design.
- Implement and test digital circuits using Altera FPGA.
- Apply hardware-software co-design techniques to develop embedded systems.
- Enhance problem-solving skills in digital system domains.

The workshop successfully enhanced the understanding of integrated learning and real-world applications of digital system design.



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between academic

Department Of Artificial Intelligence
And Data Science

WORKSHOP ON

Digital System Design Using
ALTERA FPGA



Padmanaban K
Altera Semiconductor Technology
India Pvt. Ltd.



Dr. Cyril Prasanna Raj P.
Professor,
Cambridge Institute of Technology



DR. ANTONY PRIYAKUMAR,
Drone Technology, Principal Scientist,
Cambridge Institute of Technology

 **11:00 AM**

 **JAN/28/2026**

 **Auditorium**
Jeppiar Institute of Technology



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Program Title: POSTER PRESENTATION-SIMATS

Date: 30.01.2026



SIMATS on 30 January 2026. The event provided an effective platform for students to present their innovative ideas and research work in the fields of Artificial Intelligence, Data Science, and emerging technologies.

The poster presentation showcased the active participation of students, including Vijan, Viveka, and Santo Prince of II AI&DS, who demonstrated strong technical understanding, creativity, and clarity in presenting their concepts. The Won the Prize and effectively communicated their ideas through well-structured posters and technical explanations.

Faculty members evaluated the posters based on innovation, technical depth, clarity of presentation, and visual design, and offered constructive feedback to encourage further research and improvement. The event successfully promoted research-oriented learning, analytical thinking, and professional communication skills among the students.