



NEWS Letter

Nov
2025



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DEPARTMENT NEWS

**Nov
2025**



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DEPARTMENT NEWS

DEPARTMENT OF ECE

Faculty Development Programme

Recent Trends in Prompt Engineering for Educators: Bridging AI with Academia

The poster is for a five-day faculty development programme organized by the Department of Electronics and Communication Engineering. It features the following details:

- Institution:** (An Autonomous Institution) "Self-Belief | Self-Discipline | Self-Respect"
- Address:** Kunnam, Sunguvarchatram, Sriperumbudur-631604
- Organizer:** Department of Electronics and Communication Engineering
- Programme Title:** Faculty Development Programme On Recent Trends in Prompt Engineering for Educators: Bridging AI with Academia
- Date:** 13-10-2025 to 17-10-2025
- Accreditation:** NAAC A+, IEEE, INBA, IETE
- Days and Topics:**
 - Day - 1: Topic: The Overview of Prompt Engineering
 - Day - 2: Application of Prompt Engineering
 - Day - 3: Hands-On Smart Prompting for Engineering Educators: Creating Smarter Teaching and Research Materials
 - Day - 4: Leveraging Prompt Engineering for Personalized Learning
 - Day - 5: Prompt Engineering in AI Perspective
- Faculty Members:**
 - A.Nithya (Assistant Professor - Department (CSE) St. Joseph's College of Engineering OMR, Chennai)
 - Dr G BRINDHA (Associate Professor - Department (CSE) St. Joseph's College of Engineering OMR, Chennai)
 - Dr V. Malathi (Assistant Professor-II - Department (EEE) Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya)
 - Dr Vijayakumar M (Assistant Professor - Department (CSE) SRM Institute of Science and Technology)
 - Dr M PREMKUMAR (Professor - Department (ECE) Panimalar Engineering College Poonamallee)
- Key Roles:**
 - Chief Patron:** Dr. N. MARIE WILSON (Chairman)
 - Convenor:** Dr. T. Sri Priya (HOD - ECE)
 - Faculty Coordinator:** Dr. W. Nancy (Assistant Professor - ECE)
 - Patrons:** Dr. J. Venu Gopala Krishnan (Principal), Dr. S. ShenbagaEzhil (IQAC - Director)
 - Student Coordinator:** Shalini D (Final Year - ECE Dept)
- Registration:** <https://forms.gle/T2pkpNHWgn6SG1Z46>
- Footer:** A row of 17 icons representing the Sustainable Development Goals.

The Department of Electronics and Communication Engineering organized a Five-Day Faculty Development Programme on “Recent Trends in Prompt Engineering for Educators: Bridging AI with Academia.” The programme aimed to equip faculty members with essential AI-driven prompting skills to enhance teaching and research.

Programme Highlights

- Day 1: Introduction to Prompt Engineering by Ms. A. Nithya.
- Day 2: Applications of Prompting in Education by Dr. G. Brindha.
- Day 3: Hands-on Smart Prompting Techniques by Dr. V. Malathi.
- Day 4: Personalized Learning using AI Prompts by Dr. Vijayakumar M.

- Day 5: AI Perspectives and Future Trends by Dr. Premkumar.
- Organising Team
 - Chief Patron: Dr. N. Marie Wilson
 - Patrons: Dr. J. Venu Gopala Krishnan, Dr. S. ShenbagaEzhil
 - Convenor: Dr. T. Sripriya
 - Faculty Coordinator: Dr. W. Nancy
 - Student Coordinator: Shalini D

Outcome:

The FDP provided meaningful insights into the use of AI tools for content creation, personalized learning, and research enhancement. Participants gained hands-on experience and a deeper understanding of prompt engineering's role in modern education.

Day-wise Program Schedule and Summary:

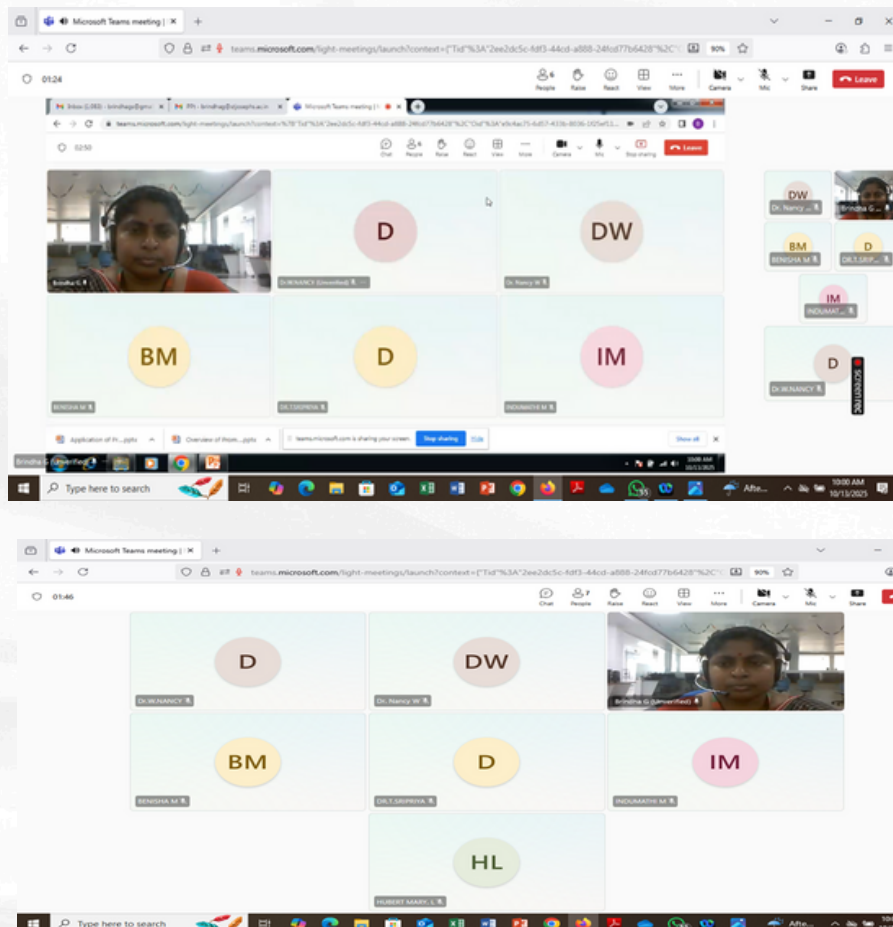
Day 1 – 13.10.2025

Topic: Overview of Prompt Engineering

Speaker:

A. Nithya -Assistant Professor / CSE

St. Joseph's College of Engineering OMR, Chennai.



The screenshot shows a Microsoft Teams meeting interface. The main window displays a presentation slide titled "PROMPT ENGINEERING TOOLS" by UPESON. The slide features a circular diagram with ten segments, each representing a different tool or concept in prompt engineering. The tools listed are: ChatGPT, Gemini, Claude, DALL-E, Midjourney, Stable Diffusion, Runway ML, Rytr, Jasper, and Grammarly. The sidebar on the right contains a grid of icons for various AI tools, including AS, BG, D, Y, IM, JM, and D, each with a brief description and a small image.

The screenshot shows a mobile application interface with a grid of icons. The icons are labeled with letters and names, such as 'AS', 'BG', 'D', 'IM', 'JS', 'BM', 'IM', 'D', and 'IM'. A red box highlights the 'BG' icon. The interface also includes a 'Sign in' button and a 'Share' button.

Day 2 – 14.10.2025

Topic: Application of Prompt Engineering

Speaker

Dr. G. Brindha -Assistant Professor / CSE

St. Joseph's College of Engineering OMR, Chennai.

FDP on Recent Trends in Prompt Engineering for Educators: Bridging AI with Academia

41:46

SEO Considerations

Search Engine Optimization

- **Keyword Optimization:** Incorporate relevant keywords like "prompt engineering," "AI communication," and "effective AI prompts" strategically throughout the content.
- **Engaging Headings and Subheadings:** Use headings and subheadings that are clear, engaging, and incorporate target keywords to improve readability and SEO ranking.
- **Internal and External Links:** Include links to related topics and authoritative sources to enhance credibility and SEO performance.
- **Mobile-Friendly Formatting:** Ensure the content is formatted for easy reading on mobile devices, as a significant portion of web traffic is mobile.
- **Use of Bullet Points and Lists:** Organize information in bullet points or numbered lists for better readability and user engagement.

Brindha G (Unlabeled)

55:26

- **Multimodal Prompting:** The future of prompt engineering may extend beyond text-based prompts to include multimodal inputs, such as a combination of text, images, and audio. Prompt engineers will need to explore how to effectively instruct AI models using diverse data types, opening up new possibilities for creative and interactive AI applications.
- **Reducing Biases:** Addressing biases within AI systems will remain a critical concern. Prompt engineering will play an essential role in developing techniques to create prompts that actively mitigate biases, ensuring that AI systems provide fair and unbiased responses across various demographics and contexts.
- **Ethical Considerations:** As prompt engineering becomes increasingly integral to AI development, ethical considerations will gain prominence. Prompt engineers will need to adhere to ethical guidelines, transparency, and accountability when crafting prompts to ensure that AI applications uphold ethical standards.

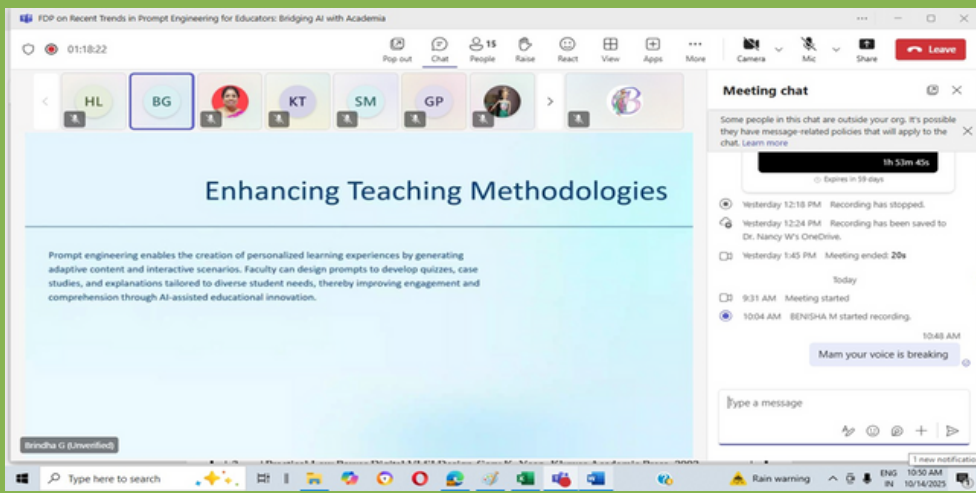
Brindha G (Unlabeled)

01:04:58

Why Prompt Engineering Matters

- Enhances AI performance without retraining models
- Saves time and resources
- Provides better control over AI behavior
- Bridges human-AI communication gap

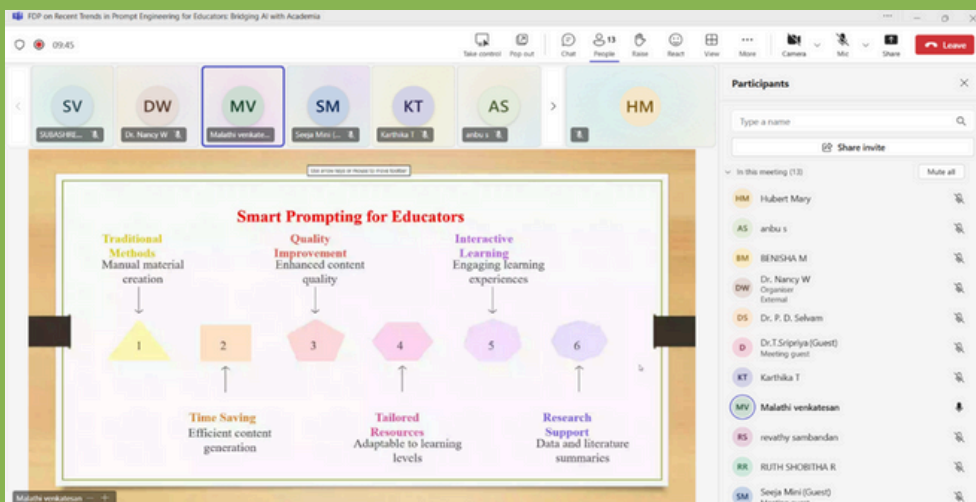
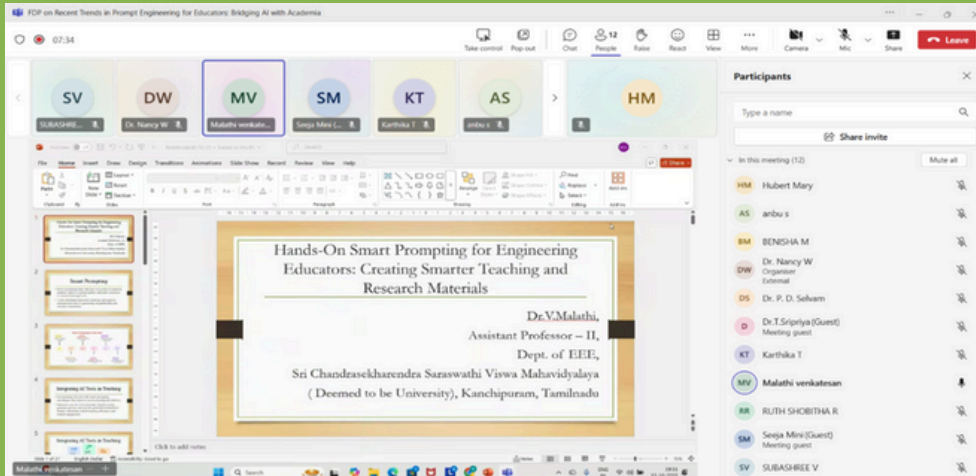
Book Requirement - Word



Day 3 – 15.10.2025

Topic: Hand-On Smart Prompting for Engineering Educators: Creating Smarter Teaching and Research Materials Speaker

**Dr. V. Malathi – Assistant Professor-II / EEE
Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya.**



10:54

Take control Pop out Chat People Raise React View More Camera Mic Share Leave

SV DW MV SM KT AS HM

Integrating AI Tools in Teaching

- Automate Tasks**
Reduces workload and frees up time for personalized attention.
- Generate Quizzes**
Provides instant assessments and feedback.
- Personalize Feedback**
Tailors learning experiences to individual student needs.

Participants

Type a name

Share invite

In this meeting (13)

Mute all

- HM Hubert Mary
- AS anbu s
- BM BENISHA M
- DW Dr. Nancy W
Organiser
External
- DS Dr. P. D. Selvam
- D Dr. T. Sripriya (Guest)
Meeting guest
- KT Karthika T
- MV Malathi venkatesan
- RS revathy sambandan
- RR RUTH SHOBITHA R
- Seela Mini (Guest)

FDP on Recent Trends in Prompt Engineering for Educators: Bridging AI with Academia

17:53

Take control Pop out Chat People Raise React View More Camera Mic Share Leave

SV DW MV SM KT AS HM

notebooklm.google.com/?utm_source=app...&referrer=https://outlook.officeapps...

NotebookLM

All Featured notebooks

Most recent + New notebook

Featured notebooks

- The Atlantic: How To Build A Life, from The Atlantic
Apr 23, 2025 - 46 sources
- Organized in Easy: Trends in Health, Wealth and...
Apr 18, 2025 - 24 sources
- The Economist: The World Ahead 2025
Jul 7, 2025 - 70 sources
- Seelma Sagar: Parenting Advice for the Digital Age
May 6, 2025 - 21 sources

Recent notebooks

Malathi venkatesan

Participants

Type a name

Share invite

In this meeting (15)

- BM BENISHA M
- DW Dr. Nancy W
Organiser
External
- DS Dr. P. D. Selvam
- D Dr. T. Sripriya (Guest)
Meeting guest
- GP GOBI P
- IM INDUMATHI M
- KT Karthika T
- MV Malathi venkatesan
- RS revathy sambandan
- RR RUTH SHOBITHA R
- SM Seela Mini (Guest)
Meeting guest
- SV SUBASHREE V

FDP on Recent Trends in Prompt Engineering for Educators: Bridging AI with Academia

24:09

Take control Pop out Chat People Raise React View More Camera Mic Share Leave

SV DW MV SM KT AS DS D RR HM

Fundamentals of Electrical and Electronic Engineering

Based on 2 sources

ELECTRONICS ENGINEERING (DC Circuits & Concepts)

- Basic Terminology & Definitions
- Circuit Elements Classification
- Circuit Laws and Principles
- Network Analysis & Theorems
- Semiconductor Devices
- Electrical Machines (Elementary Treatment)

Good content Bad content

Participants

Type a name

Share invite

In this meeting (15)

- SV SUBASHREE V
- DW Dr. Nancy W
Organiser
External
- MV Malathi venkatesan
- SM Seela Mini (Guest)
Meeting guest
- KT Karthika T
- AS anbu s
- DS Dr. P. D. Selvam
- D Dr. T. Sripriya (Guest)
Meeting guest
- RR RUTH SHOBITHA R
- HM Hubert Mary

FDP on Recent Trends in Prompt Engineering for Educators: Bridging AI with Academia

31:40

Take control Pop out Chat People Raise React View More Camera Mic Share Leave

Inbox (1,372) - vimalathir... Inbox (201) - narthan090... Fundamentals of DC Circu... Sendsteps Web Presenta... WhatsApp... Join conversation

notebooklm.google.com/notebook/d591f95a-e8bd-4410-9c43-004d57077247?authuser=1

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Fundamentals of DC Circuits and Network Theorems

Share Settings

Sources

+ Add Discover

Select all sources

- Chapter-1 (DC circuits).pdf
- Electric_Vehicles_A_Comprehensive_Rev...
- beee digital notes 2020.pdf
- p139-p142.pdf

Chat

Electrical and Electronics Engineering. The first set of excerpts provides a comprehensive review of EVs, focusing heavily on market dynamics (such as adoption rates in China and the EU), technological advancements (like lithium-ion and solid-state batteries, and hardware for DC fast and bidirectional charging), and the impact of EV integration on power grids through smart charging and Vehicle-to-Grid (V2G) technologies. The second source, a set of digital notes on electrical and electronics engineering, details fundamental concepts like network elements (active, passive, linear, unilateral), circuit analysis methods (Kirchhoff's Laws, network theorems, source transformation), and the operation of semiconductor devices (PN junction diodes, rectifiers, and Bipolar Junction Transistors). A third set of excerpts specifically addresses the challenges and necessary strategies for EV adoption in India, including the need for adequate charging infrastructure and indigenous development of Battery Management Systems (BMS) suited for local conditions.

Save to note

Video Overview Audio Overview Mind Map

Start typing...

How do electric vehicle charging standards and coordination strategies impact grid stability?

2 sources

NotebookLM can be inaccurate; please double-check its responses.

Studio

Audio Overview Video Overview

Generate an explainer video, presented to you by AI

Mind Map Reports

Flashcards Quiz

Briefing Document: Fundamentals of...
Briefing Doc - 2 sources - 1m ago

Study Guide for DC Circuits,...
Study Guide - 2 sources - 1m ago

Fundamentals of Electrical and...
2 sources - 5m ago

EV Shockwave: How...

Add note

SV MV

SM D

DS D

RR SM

BM

1/2

HM

Malathi vinkatesan

Search

ENG IN 18:35 15-10-2025

FDP on Recent Trends in Prompt Engineering for Educators: Bridging AI with Academia

47:52


Take control Pop out Chat People Raise React View More Camera Mic Share Leave

Inbox (1,374) - vimalathir... Inbox (201) - narthan090... Fundamentals of DC Circu... Sendsteps Web Presenta... WhatsApp... Join conversation

web.sendsteps.com/play/4331603/15770520

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Go to sendsteps.me and login with code Free67754667



sendsteps

SV MV

SM D

D RR

SM BM

RS

1/2

HM

Malathi vinkatesan

Search

ENG IN 18:35 15-10-2025

Day 4 – 16.10.2025

Topic: Leveraging Prompt Engineering for Personalized Learning Speaker

Dr. Vijayakumar M – Assistant Professor / CSE
SRM Institute of Science and Technology.

The screenshot shows a Microsoft Teams meeting interface. The main content area displays a presentation slide titled "PROMPT ENGINEERING". The slide lists several bullet points:

- Prompt engineering is a process of creating a set of prompts, or questions, that are used to guide the user toward a desired outcome.
- Prompt engineering is the practice of developing and optimizing prompts to efficiently use language models (LMs) for a variety of applications
- Prompt engineering is a useful skill for AI engineers and researchers to improve and efficiently use language models.
- Important for research, discoveries, and advancement
- Helps to test and evaluate the limitations of LLMs

Below the slide, a status bar indicates "Dr. M. PREMKUMAR (Unverified)". The right sidebar shows a list of participants: DP, HL, and BM. The bottom status bar shows the time as 16:18 and the date as 10/16/2025.

The screenshot shows a Microsoft Teams meeting interface in gallery view. The main content area displays a grid of participant tiles. The tiles are arranged in a 3x3 grid, with the following names visible: DW, D, DR, HL, BM, SV, KT, and SM. The bottom status bar indicates "Dr. M. PREMKUMAR (Unverified)". The right sidebar shows a list of participants: HL and BM. The bottom status bar shows the time as 50:35 and the date as 10/16/2025.

Day 5 – 17.10.2025

Topic: Prompt Engineering in AI Perspective Speaker

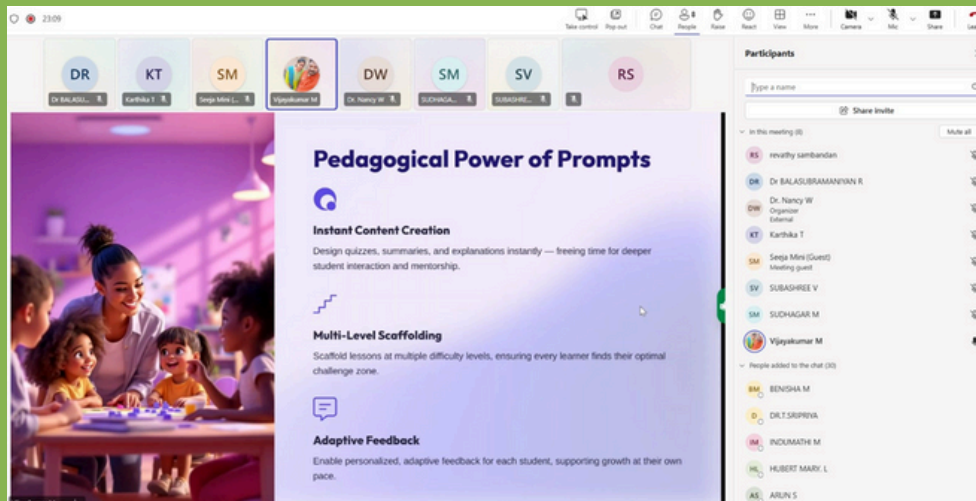
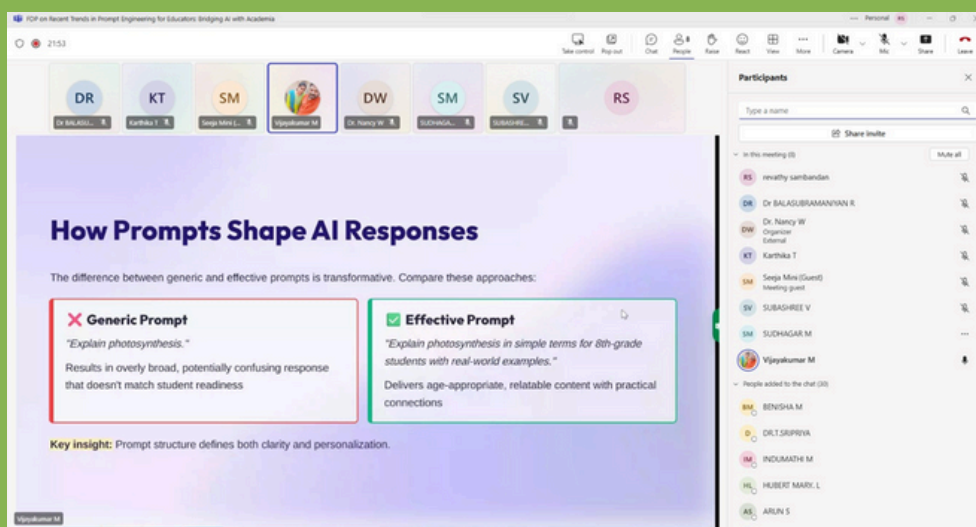
Dr. M. Premkumar – Professor / ECE
Panimalar Engineering College, Poonamallee.

The screenshot shows a Gamma presentation slide. On the left, there is a 3D illustration of a man with a beard and glasses, wearing a brown jacket, standing at a podium and speaking to an audience. The background of the illustration has the text 'PROMPT ENGINEERING FOR PERSONALIZED LEARNING'. On the right, the slide title is 'Leveraging Prompt Engineering for Personalized Learning'. Below the title, it says 'Guest Lecture by Dr. Vijayakumar M', 'Assistant Professor, Department of Computing Technologies, School of Computing, SRM Institute of Science and Technology, Kattankulathur, Chennai.', and a quote: 'Empowering Educators through AI'.

The screenshot shows a presentation slide titled 'Why Prompt Engineering Matters'. On the left, there is a photograph of a classroom where students are sitting at desks, working on laptops. On the right, there is a list of two points:

1. The rapid growth of AI tools like ChatGPT, Gemini, and Claude has transformed the educational landscape. As these powerful technologies become classroom staples, educators must learn to guide AI effectively.
2. Prompts are the new literacy for educators — the essential skill that bridges human teaching expertise with AI capabilities, enabling personalized, responsive learning experiences.

The screenshot shows a ChatGPT interface. At the top, there is a header with participant avatars: DR, KT, SM, DW, SM, SV, and RS. Below this, the ChatGPT chat window is visible. It shows a conversation where the user asks: 'Would you like me to add a simple labeled diagram (like the junction with arrows showing carrier flow) to go along with this story? It's perfect for teaching slides or classroom notes.' The chat window also shows a sidebar with options like 'New chat', 'Search chats', 'Library', and 'Projects'. On the right, there is a 'Participants' list with names and avatars: RS, KT, SM, DW, SV, and RS.



Conclusion:

The FDP was a successful initiative, providing valuable insights into Recent Trends in Prompt Engineering for Educators: Bridging AI with Academia. Participants appreciated the structured sessions, expert guidance, and hands-on exposure. The event strengthened the academic-industry linkage and contributed towards academic growth and Bridging AI.

First International Conference on Innovations in Electronics and Communication

JEPPIAAR INSTITUTE OF TECHNOLOGY
(An Autonomous Institution)
"Self-Belief | Self-Discipline | Self-Respect"
Kunnam, Sunguvarchatram, Sriperumbudur – 631 604.

ACCREDITED WITH NAAC A+
NBA NATIONAL BOARD OF ACCREDITATION CSE, IT & ECE

First International Conference on

INNOVATIONS IN ELECTRONICS AND COMMUNICATION (ICIEC'25)

7th & 8th NOVEMBER, 2025

SPEAKERS

Session Chair

Dr.K.Kausalya
Department of Computer Science and Engineering
Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology.

Key Note Speaker

Dr. Sadasivam Subbarayan
Senior Lecturer,
Department of Engineering,
University of Technology and Applied Sciences-Nizwa,
Sultanate of Oman.

Dr.N.Marie Wilson
Chairman,
Jeppiaar Institute of Technology

Dr Vijayakumar M
Department of Computer Science and Engineering
SRM Institute of Science and Technology

Ground Floor Auditorium
Organized By: Department of ECE, Jeppiaar Institute of Technology

The Department of Electronics and Communication Engineering, Jeppiaar Institute of Technology, successfully organized the First International Conference on Innovations in Electronics and Communication (ICIEC'25) on 7th and 8th November 2025. The conference provided a global platform for researchers, academicians, and industry professionals to explore emerging technologies and share innovative research in the fields of electronics and communication.

Conference Highlights

Keynote Address:

Dr. Sadasivam Subbarayan, Senior Lecturer, University of Technology and Applied Sciences–Nizwa, Sultanate of Oman, delivered an insightful keynote session on advancements shaping the future of engineering and communication technologies.

Session Chair:

Dr. K. Kausalya, Department of Computer Science and Engineering, Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology, chaired the technical sessions and facilitated research discussions.

Distinguished Speaker:

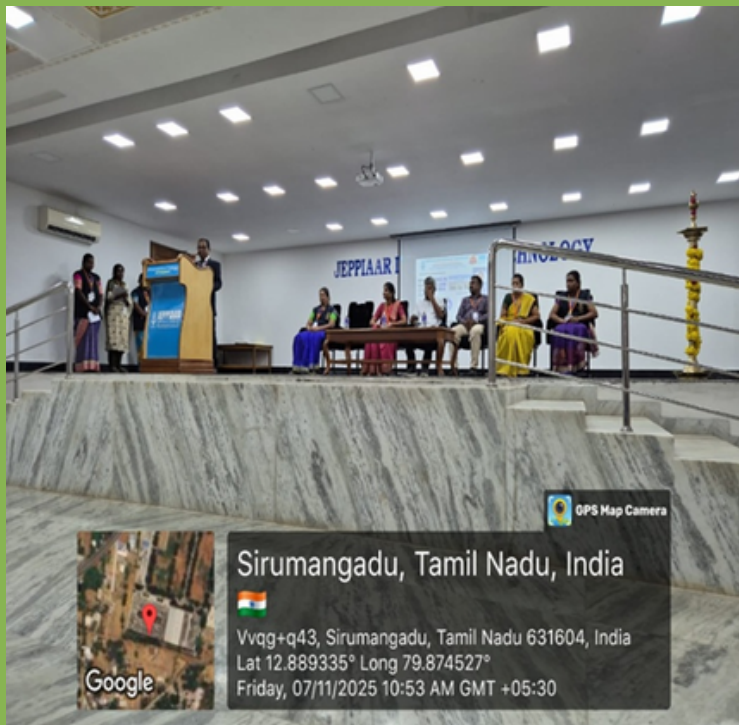
Dr. N. Marie Wilson, Chairman, Jeppiaar Institute of Technology, addressed the gathering and emphasized the importance of innovation and collaborative research.

Invited Talk:

Dr. Vijayakumar M, Department of CSE, SRM Institute of Science and Technology, shared expert insights on modern computational and communication trends.

Outcome

The conference witnessed active participation from researchers and students, fostering knowledge exchange across diverse domains. ICIEC'25 successfully created a collaborative environment promoting innovation, research excellence, and technological growth.



MoU Signing

The poster is for a Memorandum of Understanding (MoU) signing event. At the top, it features the Jeppiaar Institute of Technology (JIT) logo, its name, and the motto "Self-Belief | Self-Discipline | Self-Respect". It also mentions the address: Kunnam, Sunguvarchatram, Sriperumbudur – 631 604. Accreditation logos for NAAC (A+) and NBA (National Board of Accreditation, CSE, IT & ECE) are shown. The central text reads "Department of Electronics and Communication Engineering" and "SIGNING MOU with PRSsemicon TECHNOLOGIES PRIVATE LIMITED, CHENNAI". The date and time are "THURSDAY | 27 NOVEMBER, 2025 | 10.45AM". The PRSsemicon logo is on the left. The bottom section lists the attendees: Chief Patron Dr. N. Marie Wilson, Chairman, and Co-Patrons Dr. J. Venu GOPALA KRISHNAN (Principal), Dr. S. Shenbaga Ezhil (IQAC Director), and Mr. O. Dharani (Dean - Placement). Faculty Coordinators Dr. T. Sripriya (HOD/ECE) and Dr. M. Benisha (AP/ECE) are also listed. Logos for Mait's Innovation Cell and the Institution's Innovation Council are at the bottom. A row of 17 Sustainable Development Goals (SDGs) icons is at the very bottom.

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Kunnam, Sunguvarchatram, Sriperumbudur – 631 604.

Department of Electronics and Communication Engineering

SIGNING MOU
with
PRSsemicon TECHNOLOGIES PRIVATE LIMITED, CHENNAI

THURSDAY | 27 NOVEMBER, 2025 | 10.45AM

CHIEF PATRON
Dr. N. MARIE WILSON
CHAIRMAN

CO - PATRONS

Dr. J. VENU GOPALA KRISHNAN
PRINCIPAL

Dr. S. SHENBAGA EZHIL
IQAC DIRECTOR

Mr. O. DHARANI
DEAN - PLACEMENT

FACULTY COORDINATORS

Dr. T. SRIPRIYA
HOD/ECE

Dr. M. BENISHA
AP/ECE

17 SUSTAINABLE DEVELOPMENT GOALS

The Department of Electronics and Communication Engineering, Jeppiaar Institute of Technology, successfully signed a Memorandum of Understanding (MOU) with PRSsemicon Technologies Private Limited, Chennai on 27th November 2025. This collaboration marks a significant step towards strengthening industry-academia partnership and enhancing students' exposure to real-time industrial practices.

Purpose of the MOU

The MOU aims to:

Facilitate internships, industrial training, and technical workshops

Support skill development aligned with semiconductor and embedded system technologies

Promote collaborative projects and research opportunities

Enhance employability and industry readiness of ECE students

Event Highlights

The ceremony was graced by:

Chief Patron: Dr. N. Marie Wilson, Chairman

Patrons:

Dr. J. Venu Gopala Krishnan, Principal

Dr. S. Shenbaga Ezhil, IQAC Director

Mr. O. Dharani, Dean – Placement

Faculty Coordinators:

Dr. T. Sripriya, HOD – ECE

Dr. M. Benisha, AP/ECE

Representatives from PRSsemicon Technologies shared insights on the growing semiconductor ecosystem and emphasized the importance of industry collaboration in shaping future-ready engineers.

Outcome

The MOU is expected to enrich academic learning through practical exposure, foster innovation, and open avenues for student internships, technical training, and collaborative research activities.





COLLABORATIVE ACTIVITY

The Department of Electronics and Communication Engineering, Jeppiaar Institute of Technology, successfully organized a collaborative workshop titled “Innovate & Integrate: Semicon Frontiers! Bridging Industry and Academia” on 27 November 2025 from 10.45 AM to 11.45 AM.

This enriching session was conducted in association with PRSsemicon Technologies Pvt. Ltd, featuring distinguished industry experts:

Mr. Yuvaraj Arunachalam, Founder & Chairman

Mr. Ranjith Kumar TC, Director

Ms. Vinithra, Director – HR

The workshop provided students with valuable insights into semiconductor innovations, industry expectations, and emerging opportunities in the electronics domain. The speakers emphasized the importance of industry-academia collaboration, skill development, and adopting cutting-edge technologies to stay aligned with global semiconductor advancements.

The event was coordinated by:

Student Coordinator: Ms. A. Ajoy Blessy, III ECE

Faculty Coordinator: Mr. M. Sudhagar, Assistant Professor/ECE

The session was conducted under the guidance of

Dr. T. Sripriya, Head of the Department, Associate Professor/ECE.

The department extends its sincere thanks to PRSsemicon Technologies Pvt. Ltd for their participation and invaluable contribution to student learning.



Kunnam, Tamil Nadu, India 🇮🇳
Jeppiaar Institute of Technology, Kunnam, Sriperumbudur,
Kunnam
44P N 377813.56 1425176.92
27/11/2025 11:20 AM India Standard Time



Kunnam, Tamil Nadu, India 🇮🇳
Jeppiaar Institute of Technology, Kunnam, Sriperumbudur,
Kunnam
44P N 377813.56 1425176.92
27/11/2025 11:21 AM India Standard Time



Kunnam, Tamil Nadu, India 🇮🇳
Jeppiaar Institute of Technology, Kunnam, Sriperumbudur,
Kunnam
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27/11/2025 11:20 AM India Standard Time



Kunnam, Tamil Nadu, India 🇮🇳
Jeppiaar Institute of Technology, Kunnam, Sriperumbudur,
Kunnam
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27/11/2025 10:47 AM India Standard Time

ENERGY CONSERVATION EVENT

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Ministry of Education
Government of India

NAAC
A+

NBA
NATIONAL BOARD
ACCREDITED
CSE, IT & ECE

INSTITUTION'S
INNOVATION
COUNCIL
Ministry of Education, India

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING PROUDLY PRESENTS**

**WEBINAR ON
Innovation in Energy Conservation**
BY

Register here

MR.SARATHBABU B.E.,M.E
SRI EASWARI SCIENTIFIC
SOLUTION PVT LTD
PRODUCTION COORDINATOR

G-MEET LINK

Head of the department
**Dr. T. Sripriya Associate
Professor/ECE**

Student Coordinator
**SRIRAM V
II - YEAR/ECE**

28/11/2025

2.00 pm - 3.00 pm

Mode : Virtual Mode.

EVENT COORDINATORS
Ms.M.Indhumathi - AP/ECE
Ms.M.Nithya - AP/ECE

"SAVE.SUSTAIN.SURVIVE"

SUSTAINABLE DEVELOPMENT GOALS

The Department of Electronics and Communication Engineering, Jeppiaar Institute of Technology, organized a webinar titled “Innovation in Energy Conservation” on 28/11/2025 from 2:00 pm to 3:00 pm via virtual mode. The event was proudly presented as part of the department’s academic activities, aligning with Sustainable Development Goals.

Key Details of the Webinar

- Speaker: Mr. Sarathbabu B.E., M.E.
Production Coordinator, Sri Easwari Scientific Solution Pvt Ltd.
- Theme: “SAVE.SUSTAIN.SURVIVE” focusing on innovations in energy conservation.
- Head of Department: Dr. T. Sripriya, Associate Professor/ECE.
- Student Coordinator: Sriram V, II-Year ECE.
- Event Coordinators: Ms. M. Indhumathi (AP/ECE) & Ms. M. Nithya (AP/ECE).

Objectives

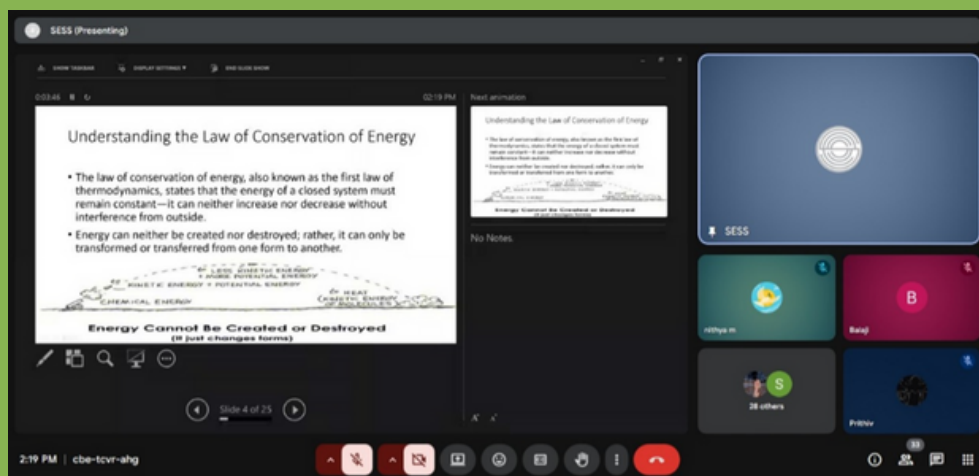
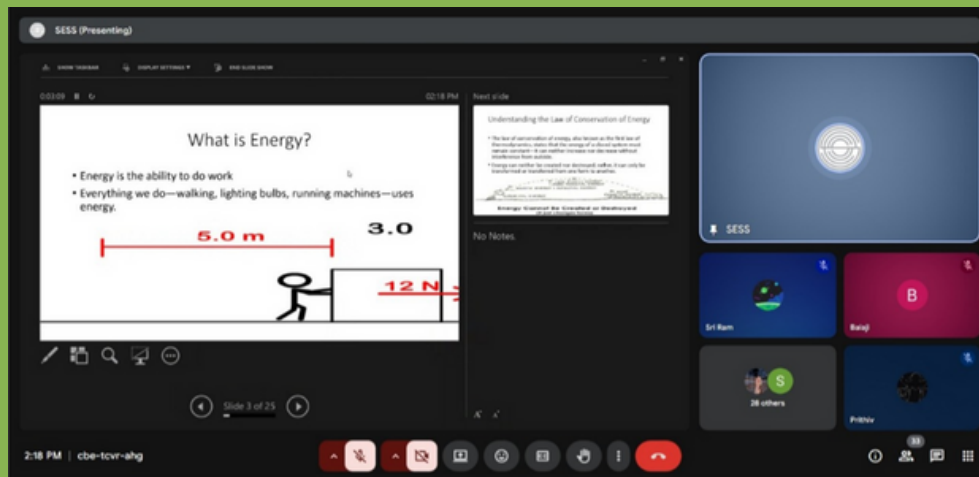
1. To explore recent advancements in energy conservation technologies.
2. To inspire students and professionals toward sustainable energy practices.
3. To bridge the gap between industry innovations and academic learning.

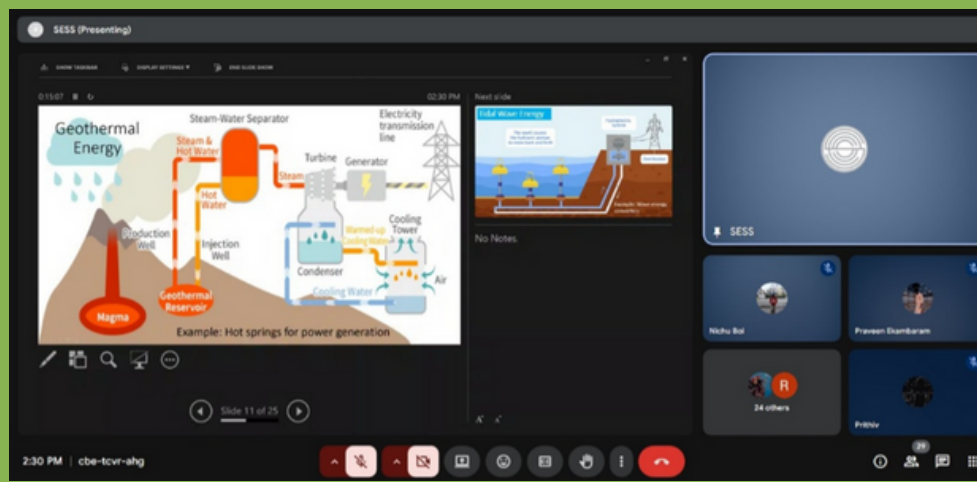
Proceedings

The webinar commenced on time with an introduction by the Head of the Department, Dr. T. Sripriya, who highlighted the importance of energy conservation in modern engineering. Mr. Sarathbabu delivered an insightful session covering innovative techniques, industrial applications, and future trends in energy management. The session encouraged active participation through Q&A, linking the discussions to the UN’s Sustainable Development Goals.

Outcome

Participants gained valuable knowledge on energy-efficient solutions and their real-world implementation. The event reinforced the institution's commitment to promoting sustainable innovation among students and faculty. The webinar was successfully conducted with enthusiastic engagement and achieved its objective of spreading awareness about energy conservation innovations.





WORKSHOP


JEPPIAAR INSTITUTE OF TECHNOLOGY
(An Autonomous Institution)
"Self-Belief | Self-Discipline | Self-Respect"
 Kunnam, Sunguvarchatram, Sriperumbudur – 631 604.
 












DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
PROUDLY PRESENTS
WORKSHOP ON DESIGNING AND BUILDING PROTOTYPE INTO STARTUPS


10:00 AM - 11:30 AM


Mr. J. ANTONY JEFFREY
 AUDIO/SOUND ENTHUSIAST


29/11/2025

CHIEF PATRON
DR.N.MARIE WILSON
 CHAIRMAN

HEAD OF THE DEPARTMENT
Dr. T. SRIPRIYA
 Associate Professor, ECE

PRINCIPAL
DR.J VENU GOPALA KRISHNAN

IQAC DIRECTOR
Dr.S.SHENBAGA EZHIL

FACULTY COORDINATORS
Ms.M.INDUMATHI, AP/ECE
Ms.M. NITHYA, AP/ECE



The Department of Electronics and Communication Engineering, Jeppiaar Institute of Technology, organized a workshop titled “Designing and Building Prototype into Startups” on 29/11/2025 from 10:00 AM to 11:30 AM. The event was conducted under the aegis of the Institution’s Innovation Council (IIC) and supported by various national bodies such as ISO, AICTE, and AWS.

Chief Patron: Dr. N. Marie Wilson (Chairman)

Principal: Dr. J. Venu Gopala Krishnan

Head of Department (ECE): Dr. T. Sripriya (Associate Professor)

IQAC Director: Dr. S. Shenbaga Ezhil

Faculty Coordinators: Ms. M. Indumathi, AP/ECE & Ms. M. Nithya, AP/ECE

The workshop featured Mr. J. Antony Jeffrey, an audio/sound enthusiast, who guided participants on transforming innovative ideas into viable prototypes and startups. The session emphasized practical aspects of product design, prototyping techniques, and entrepreneurial skills essential for engineering students.

The event saw active participation from ECE students and faculty, fostering an environment of innovation and startup culture aligned with the Sustainable Development Goals (SDGs). The workshop concluded with an interactive Q&A, inspiring attendees to engage in research, development, and entrepreneurship.

Outcomes:

The outcomes of the workshop “Designing and Building Prototype into Startups” typically include:

- 1. Skill Development – Participants learn practical techniques for designing and prototyping products suitable for startups.**
- 2. Entrepreneurial Insight – Exposure to the process of turning an idea into a viable startup, including ideation, validation, and MVP (Minimum Viable Product) creation.**
- 3. Networking – Opportunities to connect with faculty, industry experts (like the audio/sound enthusiast Mr. J. Antony Jeffrey), and peers interested in innovation.**
- 4. Prototype Creation – Hands-on experience in building a prototype that can be tested or pitched for startup funding.**
- 5. Industry Awareness – Understanding of modern tools and collaborations (e.g., AWS, IDEALab) used in product development and startup ecosystems.**
- 6. Certification – Often, participants receive a participation certificate that can add value to their academic or professional profile.**



6G Emerges as The World's Most Rapidly Advancing Technology, Transforming the Future



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The global engineering and technology community is witnessing a major shift as 6G technology rises to become the most dominant and fastest-growing research area in the field of Electronics and Communication Engineering (ECE). With nations already accelerating beyond 5G adoption, 6G has moved from concept to active development, prompting worldwide excitement across academia, industry, and government sectors.

According to leading research institutes and industry reports, 6G promises data speeds up to 1 Tbsp., sub-millisecond latency, AI-native communication networks, and seamless integration of sensing, imaging, and communication into a single platform. These advancements position 6G not merely as a next-generation communication system, but as a foundation for intelligent, interconnected, and autonomous technologies of the future.

In India, major institutions including IIT Hyderabad, IIT Madras, and private telecom companies have already begun testing Beyond-5G (B5G) and early 6G technologies, marking a significant step in aligning with global progress. Experts predict that India will become a major contributor in 6G hardware design, semiconductor development, and AI-driven communication systems by 2030.